

# ESOSEG

Environment for Simulation, Operation  
and Optimization of Smart Energy Grids

Hochschule Ulm



CIMug Meeting 2017 - Herzogenaurach

## Open-Source Middleware Platform for the data exchange within a Distribution Grid Operator

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14.06.2017



## Agenda

- Project partners & objectives
  
- Software architecture:
  - Architectural patterns and principles
  - Adapter Generation
  - Prototype Implementation
  
- Utilization of CIM:
  - CIM mapping
  - Next steps
  - Open discussion

## Project partners

- University of Applied Science Ulm (HSU)
- SEKAS GmbH
- Technical University Munich (TUM)

### Associated Partners:

- Stadtwerke Ulm (SWU)
- Stadtwerke Pfarrkirchen (SWPAN)
- Hessware GmbH

**Project duration:** 3 years

01.10.2015 – 30.09.2018

**Funding agency:** Federal Ministry of Economic Affairs and Energy (BMWi)  
(Promotion Reference Number 0325811C)

## Project objectives

### Background:

- High installation of distributed RE generation in the distribution grids
- High need for systematic grid analysis and data exchange

### Main objective:

To develop a flexible and expandable platform, in order to ease the analysis of distribution grid structures, detect the critical grid conditions and investigate the possible grid reinforcement measures and their feasibility

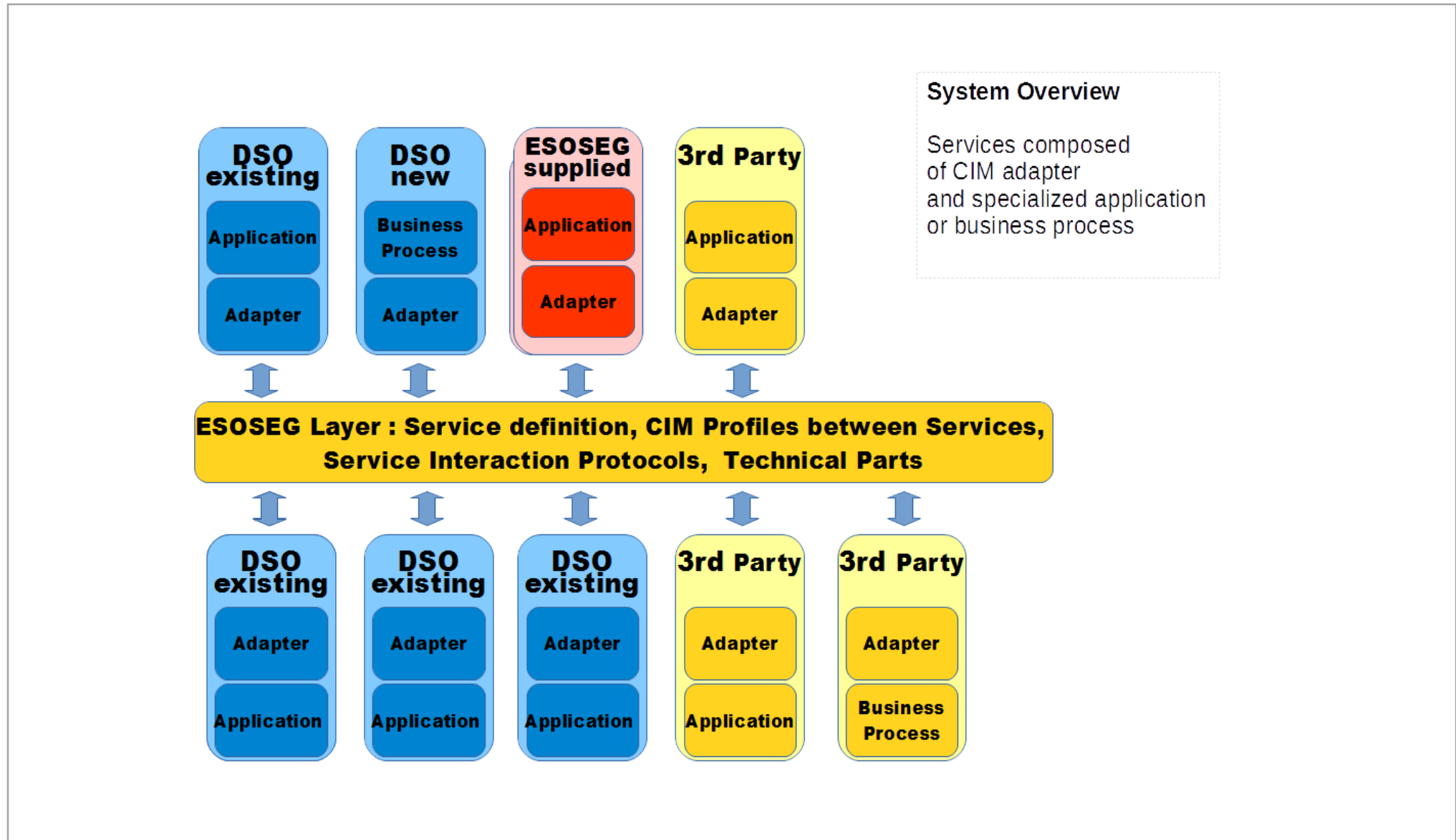
### Requirements of the platform:

- Standardized information modelling for the data exchange
- Cost savings for DSOs → open-source framework
- Simple integration to the existing IT environment of the DSO

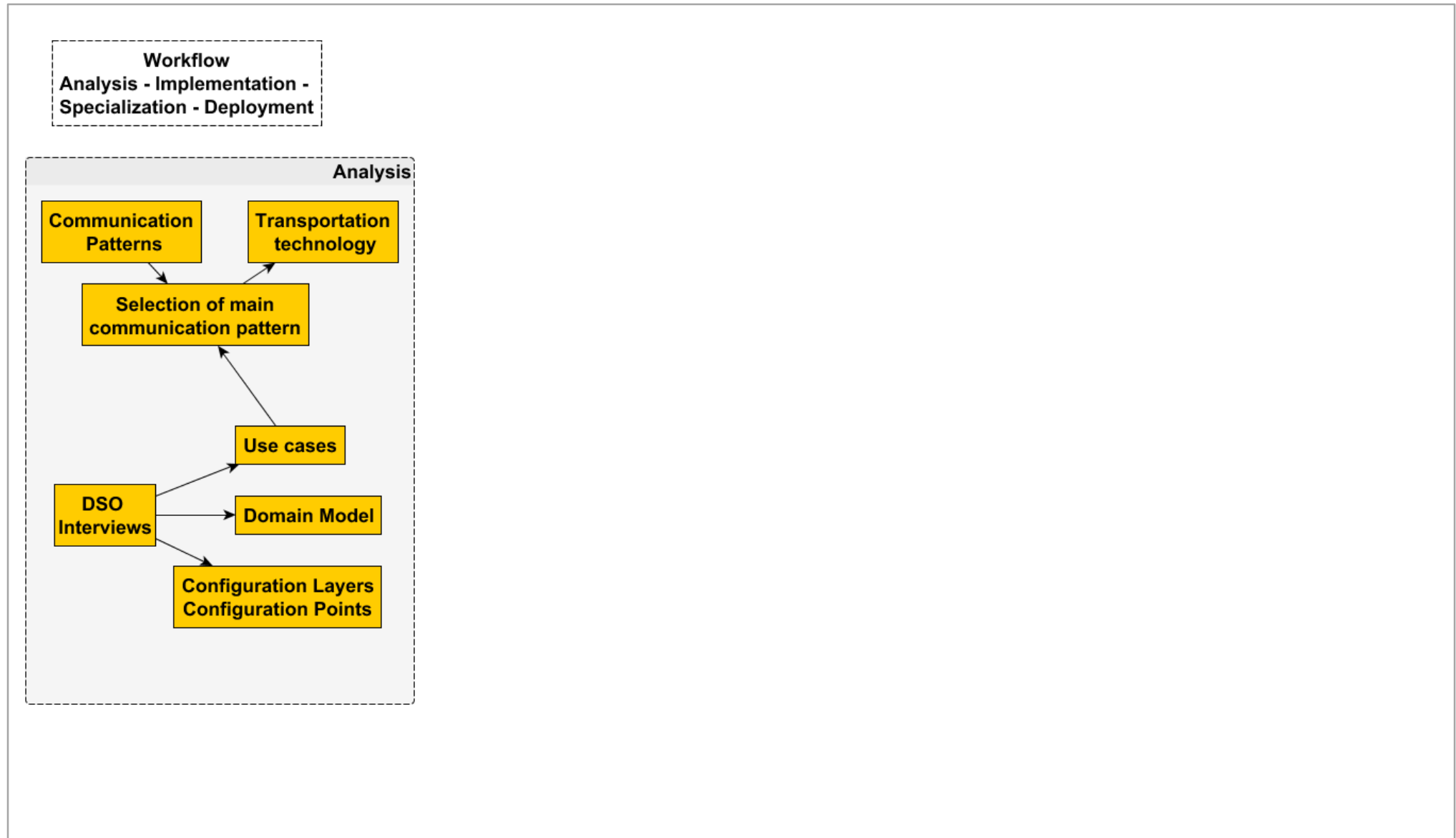
## Architectural patterns and principles

- Lean microservices – ready for mobile, desktop, application server, cloud
- REST-based - proven technology stack
- No central database, data mastership remains in services – distributed system
- CIM-based data exchange between services – open for 3rd parties
- CIM adapters by model-based generation
- Existing applications wrapped in services
- Simple basic services (reusable),  
complex business case services (specific)
- External webservices can be integrated

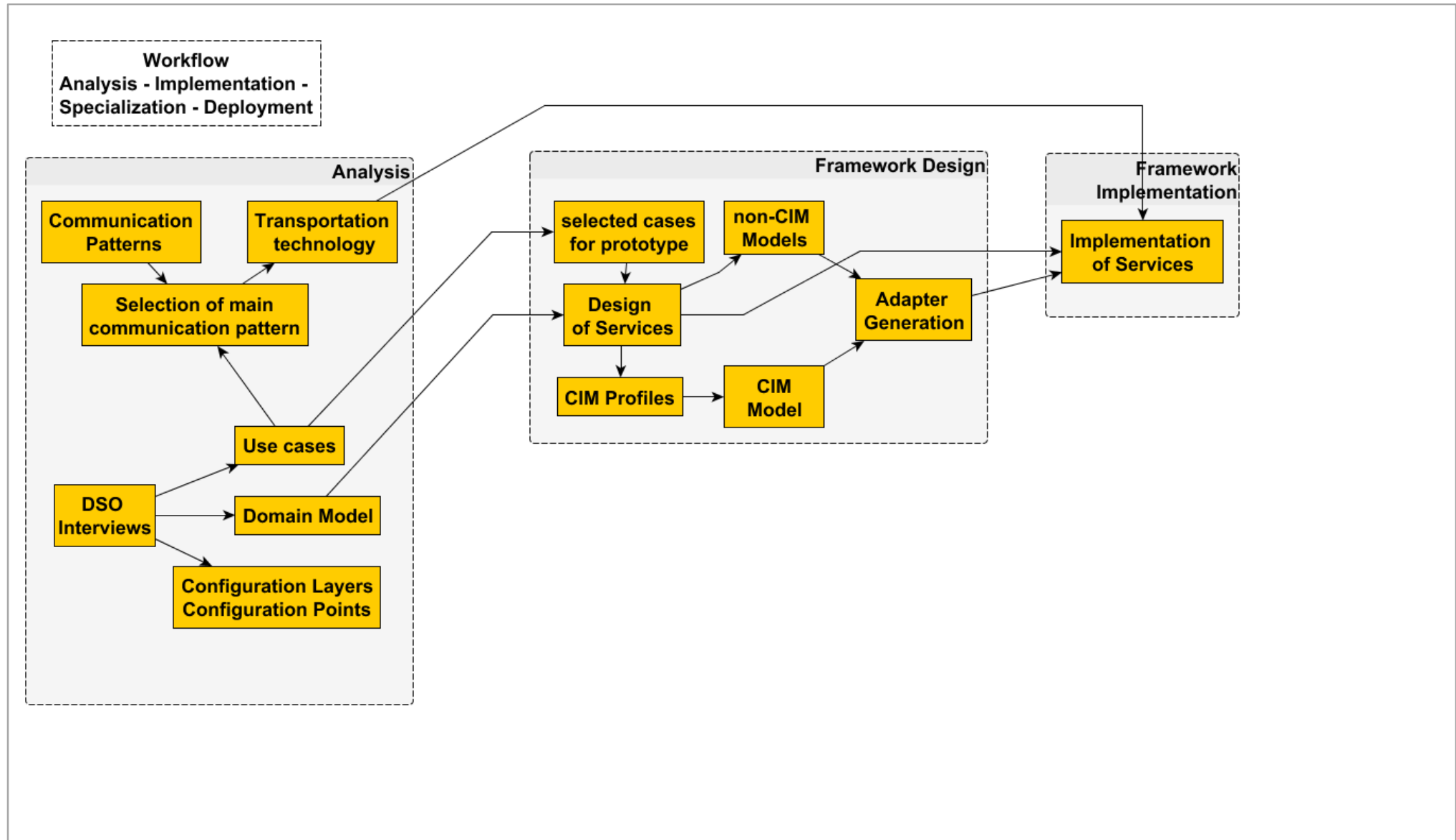
## System Overview



## Development Workflow

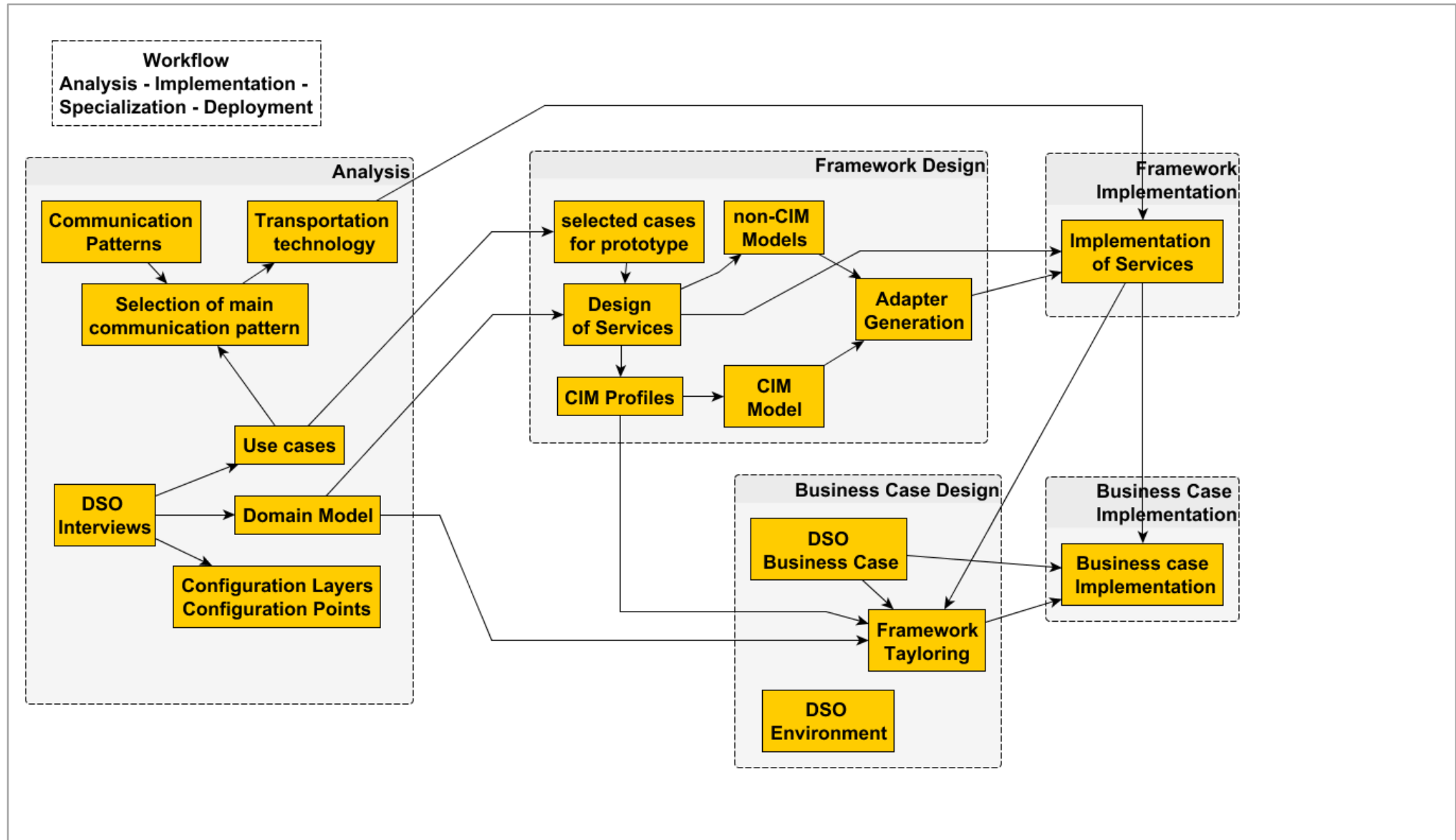


## Development Workflow

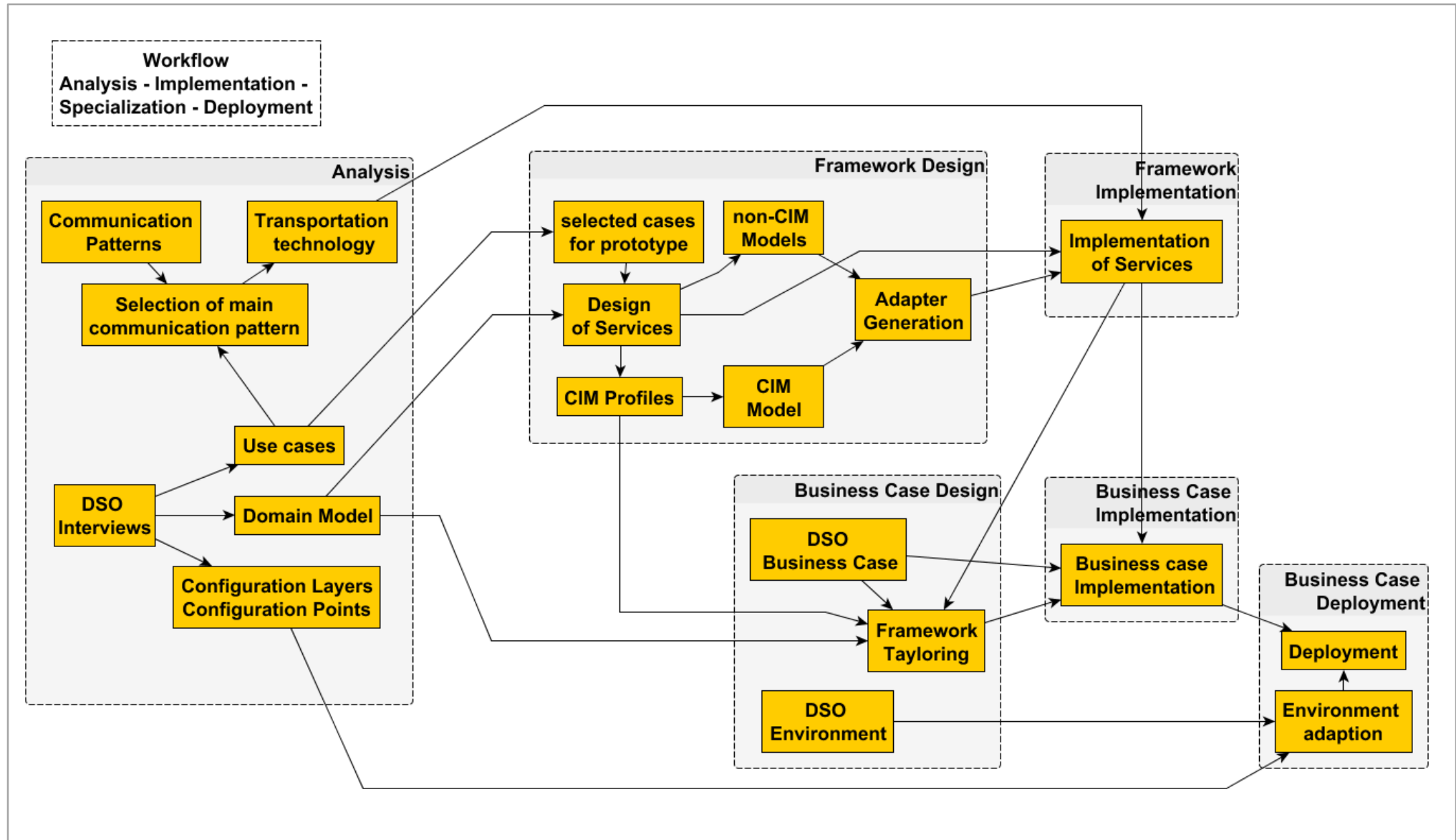




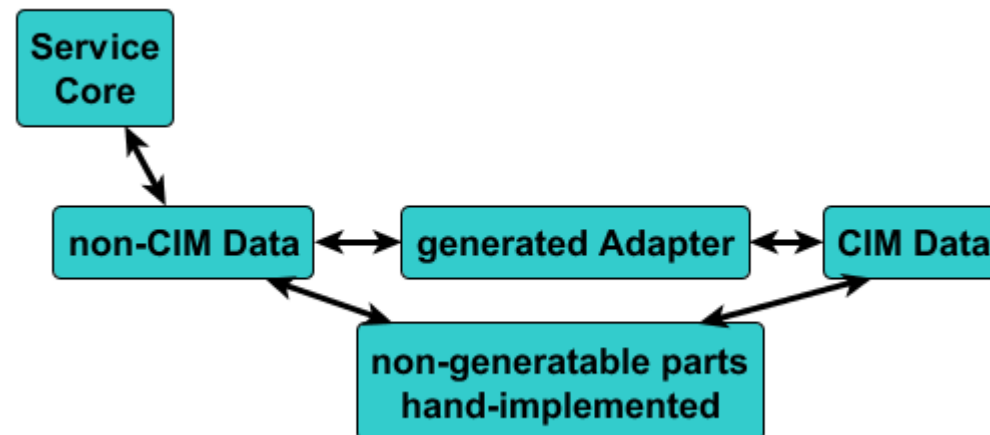
## Development Workflow



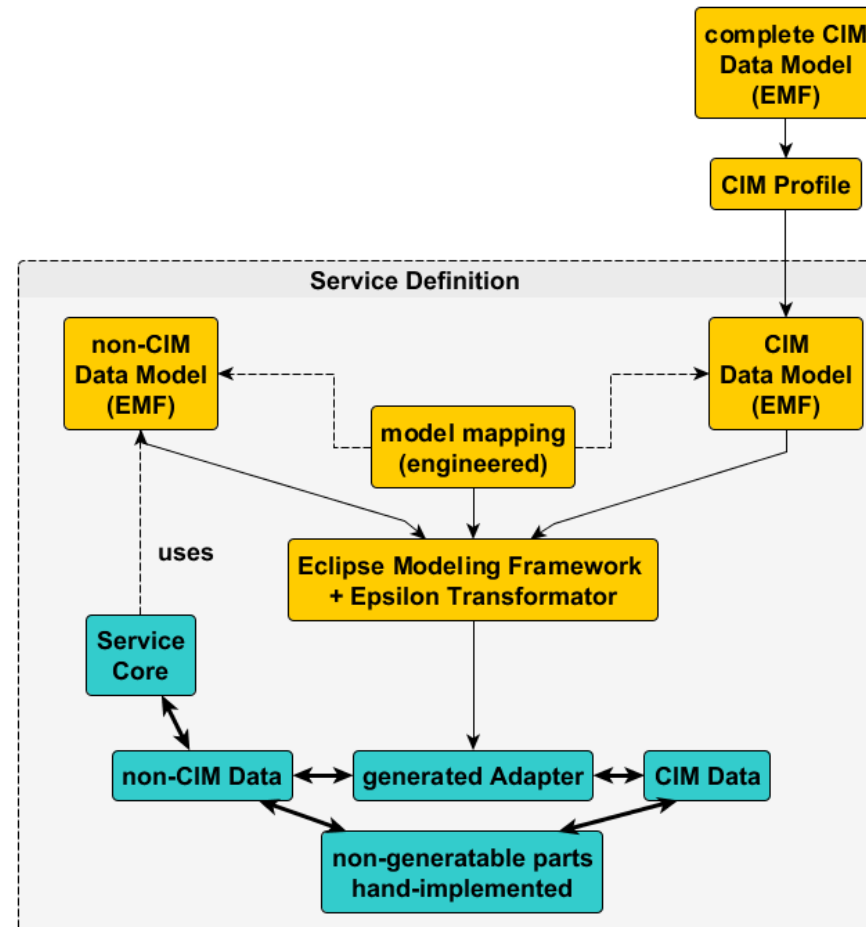
## Development Workflow



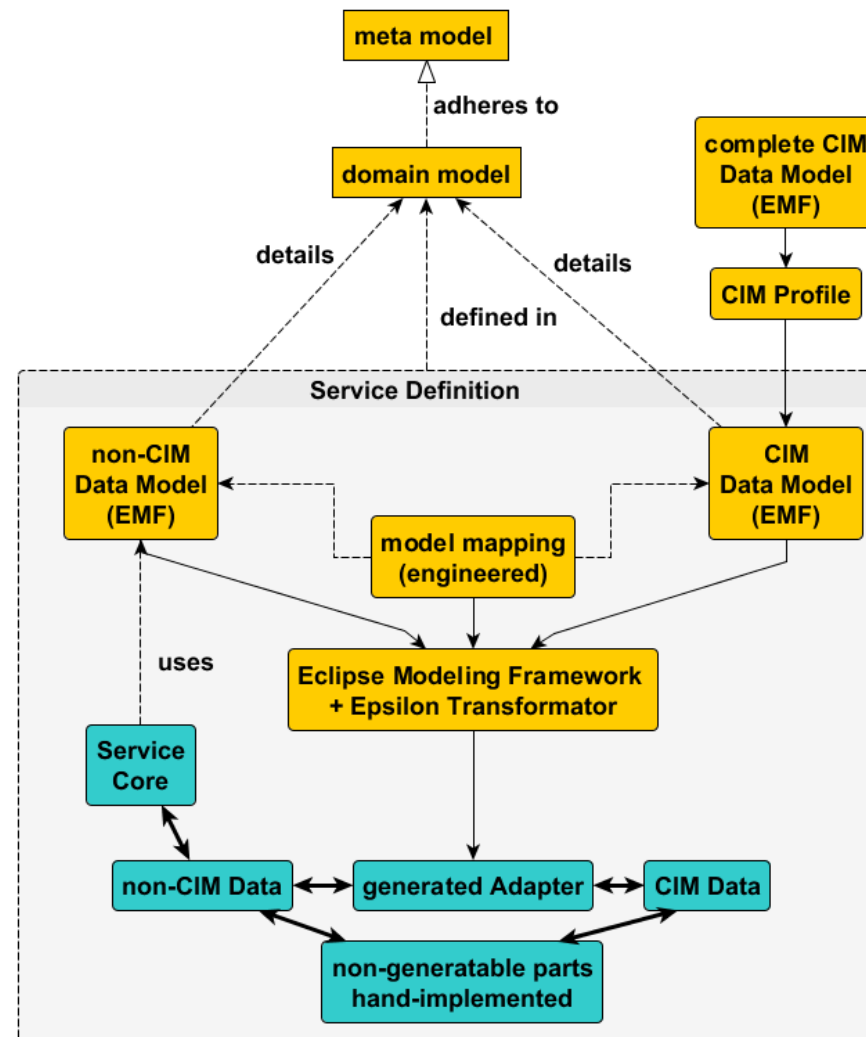
## Adapter Generation



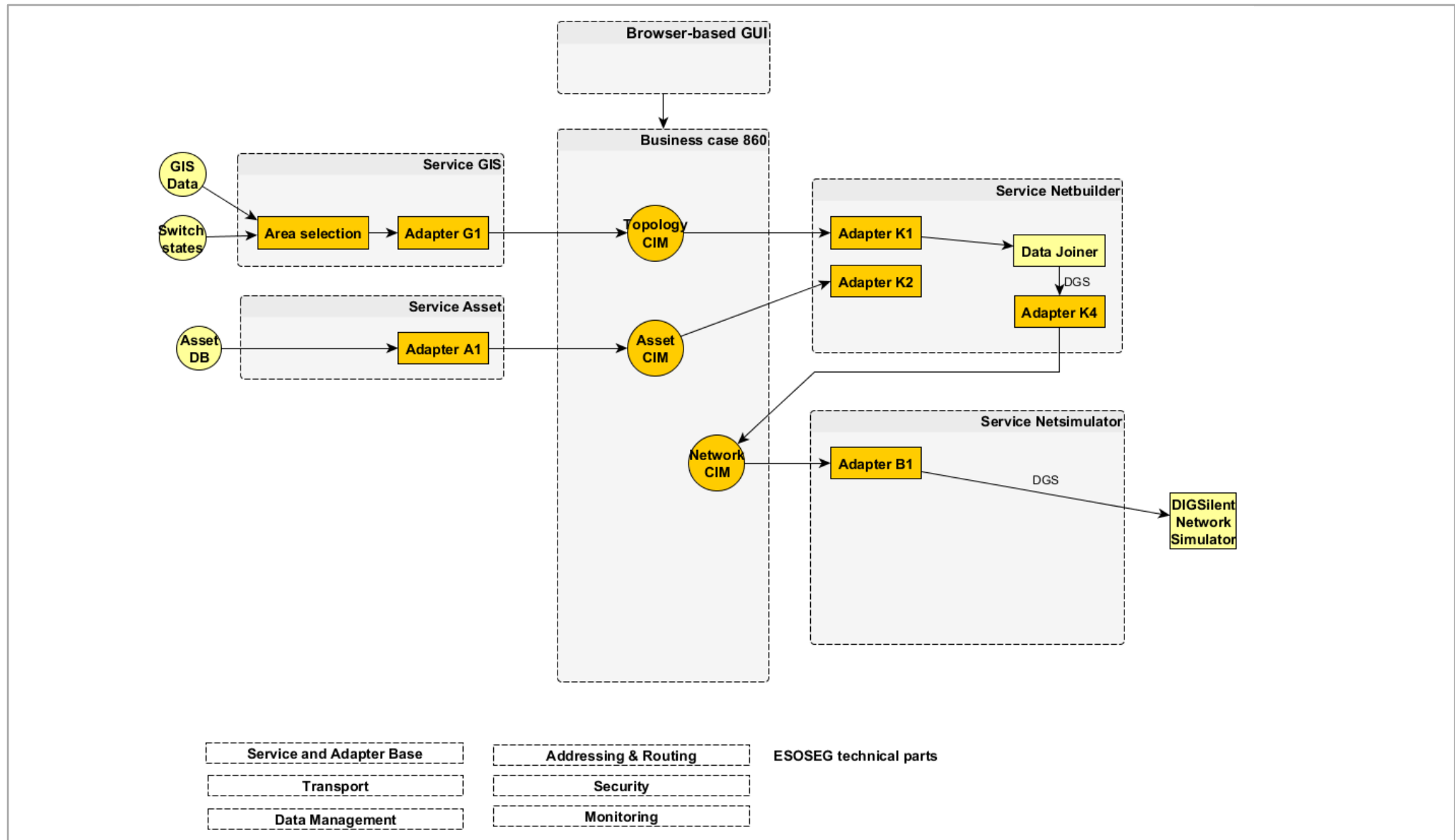
## Adapter Generation



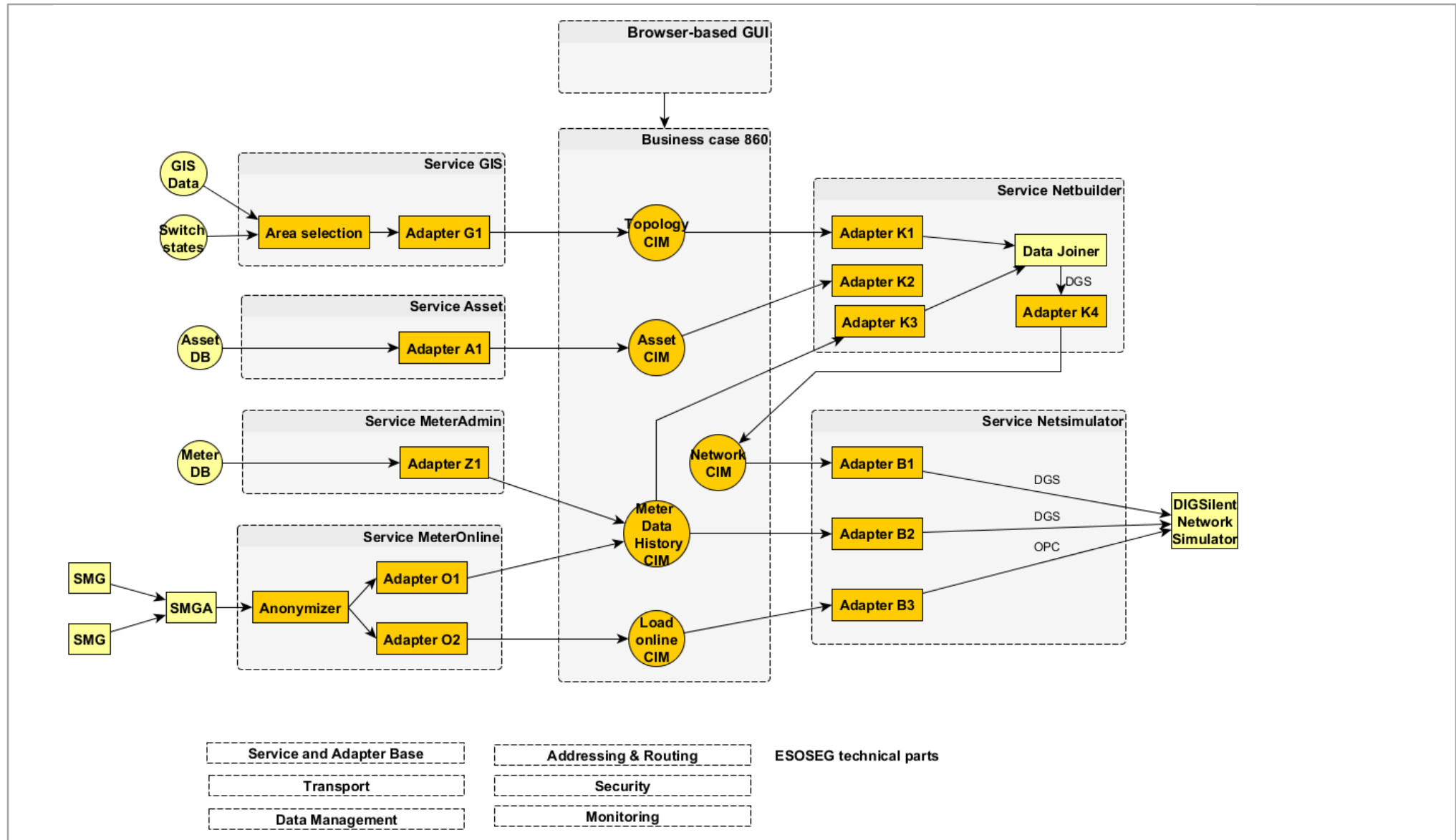
## Adapter Generation



## Prototype Implementation



## Prototype Implementation



## Implementation summary

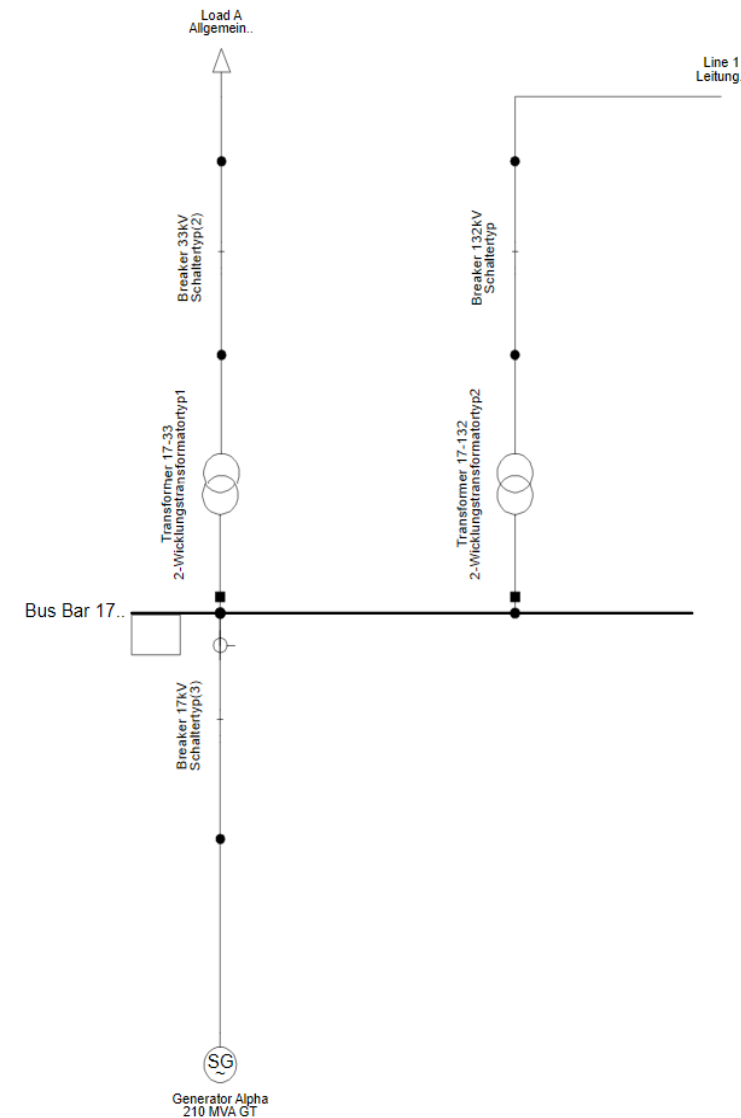
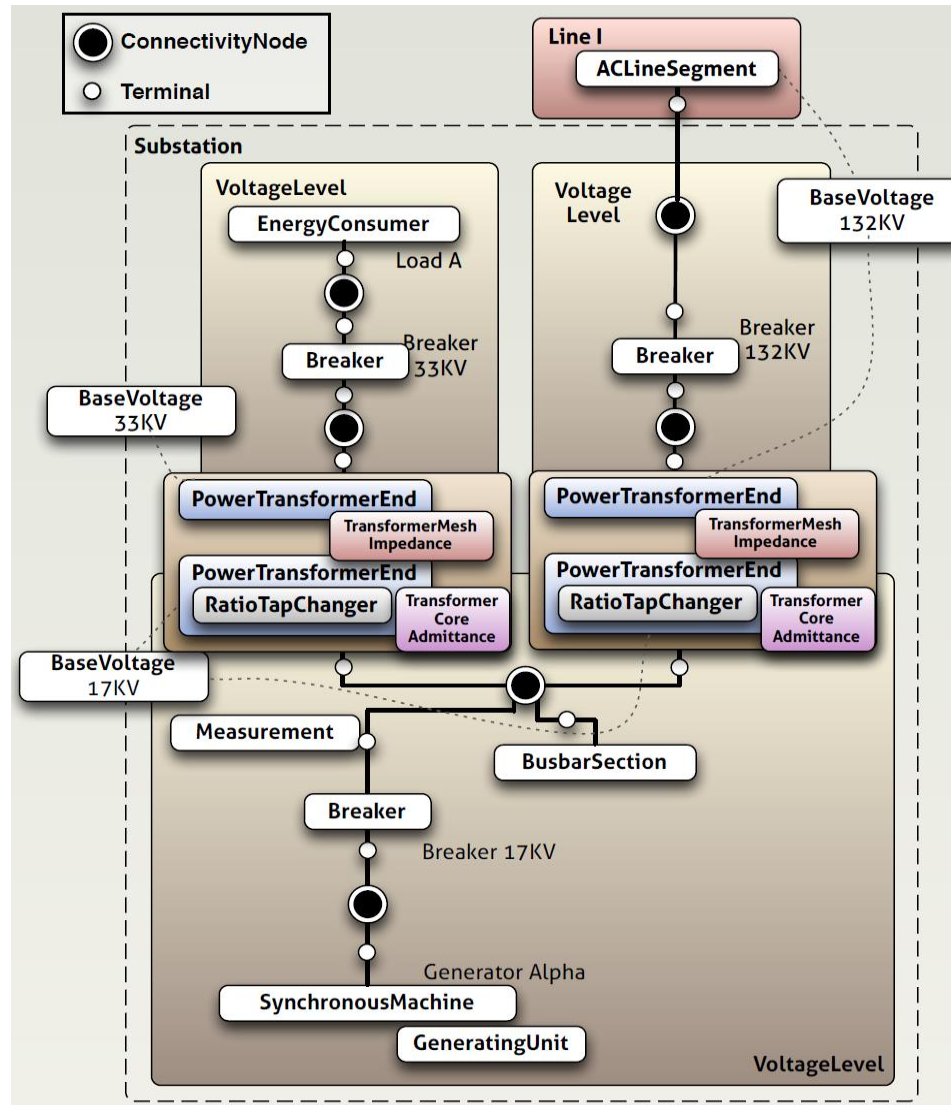
- Domain model as base for technical decisions, service cut, CIM profiles and choice of prototype scenarios
- Domain model as base for business case tailoring and well-defined concretization points for deployment
- Wrappers for business applications, no big bang integration
- Business alignment possible: Service – Data Responsibility – Department, e.g. Asset or GIS



## CIM mapping

- Hand-implemented mapping environment:  
Python based on PyCIM with CIM 15
- Implemented CIM adapters:
  - Mapping of LV grid model in DGS format from DIgSILENT
  - Mapping of topological grid data derived from GIS system
  - Mapping of asset data obtained from asset data bank

## CIM mapping: Example



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# CIM mapping: Example

■ First step:

Specification of the  
 required CIM classes

| Physical Components   | DGS Components  | CIM Instances  | CIM Instances with logic  | CIM Instances for additional parameters      |
|-----------------------|-----------------|--|---|--|
| General Info          | General         | –  | –   |  |
|                       | ElmNet          | 1- EquivalentNetwork   | –   |  |
| Synchronous Generator | ElmSym (row)    | 1. GeneratingUnit<br>2. SynchronousMachine   | BaseVoltage, VoltageLevel   |  |
|                       | TypSym (row)    | –  | –   |  |
| Breaker               | ElmCoup (row)   | 1. Breaker   | BaseVoltage, VoltageLevel   | Asset  |
|                       | TypSwitch (row) | –  | –   |  |
| Current Transformer   | StaCt (row)     | 1. CurrentTransformer<br>2. Measurement  | –   |  |
| Power Transformer     | ElmTr2 (row)    | 1. PowerTransformerEnd<br>2. RatioTapChanger<br>3. TransformerMeshImpedance<br>4. TransformerCoreAdmittance<br>5. PowerTransformerEnd<br>6. PowerTransformer | BaseVoltage, VoltageLevel<br><br>BaseVoltage, VoltageLevel                          | Asset  |
|                       | TypTr2 (row)    | –  | –   |  |
| Load                  | ElmLod (row)    | 1. EnergyConsumer<br>2. LoadResponseCharacteristic   | BaseVoltage, VoltageLevel   |  |
|                       | TypLod (row)    | –  | –   |  |
| Line                  | ElmLne (row)    | 1. ACLineSegment<br>2. Line  | BaseVoltage, VoltageLevel   | ConductorInfo<br>WireArrangement<br>WireType |
|                       | TypLne (row)    | –  | –   |  |
| Node                  | ElmTerm (row)   | 1. ConnectivityNode  | if the node is a bus bar:<br>BusbarSection<br>Terminal<br>BaseVoltage, VoltageLevel |  |
| Connection            | StaCubic (row)  | 1. Terminal  | Terminal for a bus bar  |  |
| Graphical Info        | IntGrf          | Not considered yet   |   |  |
|                       | IntGrfcon       |  |   |  |
|                       | IntGrfnet       |  |   |  |

## CIM mapping: Example

- Second step: mapping of element's parameters

| DGS - CIM                    |               |              |                   |                                |
|------------------------------|---------------|--------------|-------------------|--------------------------------|
| DGS Components               | DGS Parameter | Mapping Type | CIM Parameter     | CIM Classes                    |
| StaCubic<br>(row of a table) | ID            | Direct       | mRID: String      | Terminal                       |
|                              | loc_name      | Direct       | name: String      |                                |
|                              | chr_name      | Not mapped   |                   |                                |
|                              | obj_bus       | Indirect     | aliasName: String | Associated<br>ConnectivityNode |
|                              | fold_id       | Direct       | mRID: String      |                                |
|                              | obj_id        | Direct       | mRID: String      |                                |
|                              |               |              |                   |                                |

Mapping of CIM Terminal: relatively direct mapping

# CIM mapping: Example

## ■ Second step: mapping of elements parameters

### ➤ Mapping of transformer

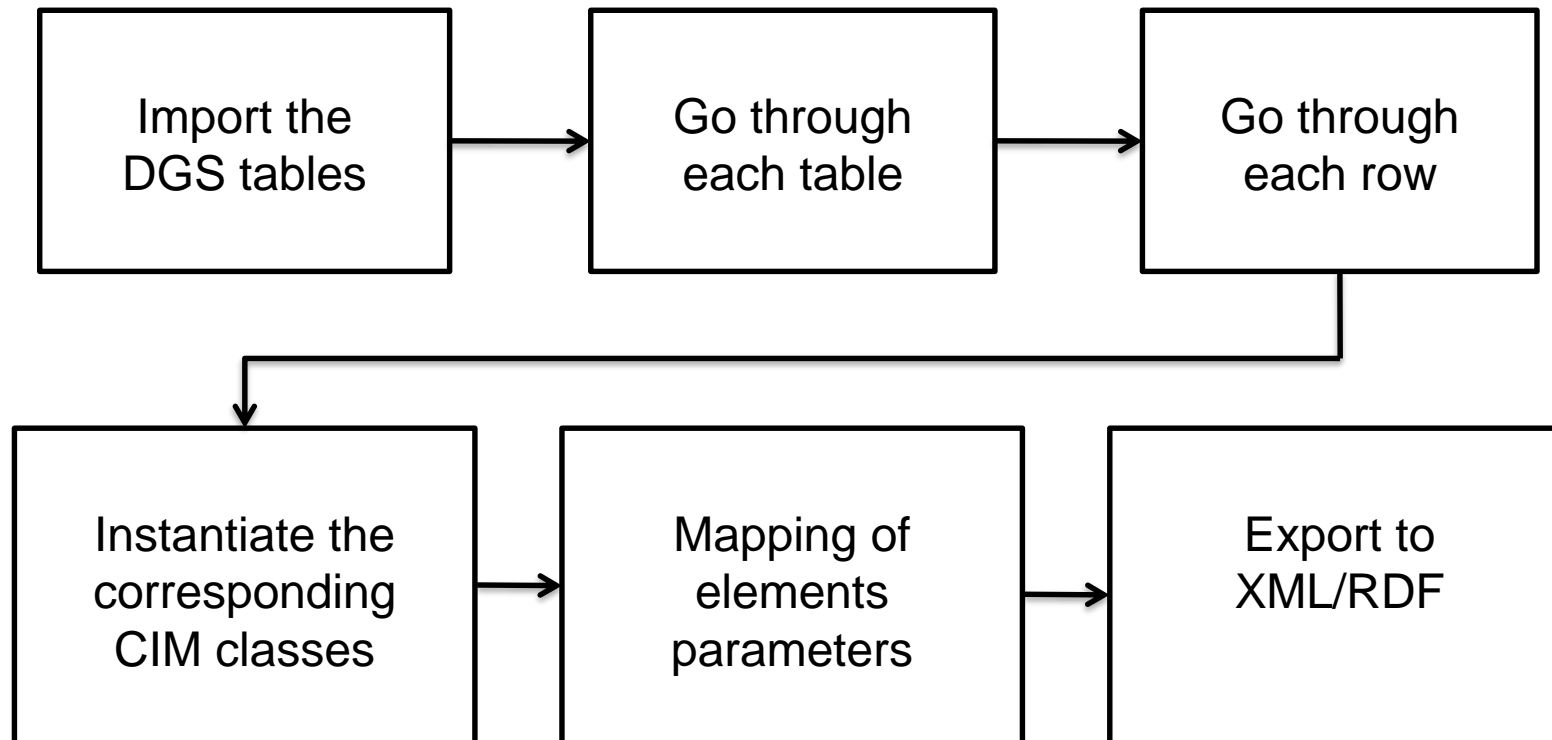
- ❖ Mainly indirect mapping
- ❖ Mathematical representation is different:

- in CIM (R, X, C)
- in DGS (open circuit and short circuit test parameters)

| DGS - CIM       |                |                                  |  |   |                                    |
|-----------------|----------------|----------------------------------|--|---|------------------------------------|
| DGS Komponenten | DGS Parameter  | Kommentare                       | CIM Parameter                          | CIM Klassen   |                                    |
| TypTr2          | ID(a:40)       | keine direkte Mapping            | mRID: String                           | PowerTransformer  |                                    |
|                 | loc_name(a:40) | direkte Mapping                  | name: String                           |   |                                    |
|                 | typ_id(p)      | keine direkte Mapping            | aliasName: String                      |   |                                    |
|                 | outserv(l)     | direkte Mapping                  | normallyInService: Boolean             |   |                                    |
|                 | u_auto         | nicht relevant/ nicht gefunden   |  |   |                                    |
|                 | chr_name(a:20) | nicht relevant/ nicht gefunden   |  |   |                                    |
|                 | cgnd_h         | nicht relevant/ nicht gefunden   |  |   |                                    |
|                 | cgnd_l         | nicht relevant/ nicht gefunden   |  |   |                                    |
|                 | rtafac         | nicht relevant/ nicht gefunden   |  |   |                                    |
|                 | fold_id(p)     | keine direkte Mapping            | mRID: String                           |   |                                    |
|                 | sernum(a:20)   | direkte Mapping                  | serialNumber: String                   |   |                                    |
|                 | const(r)       | direkte Mapping                  | installationDate: String               |   |                                    |
|                 | mntag(i)       | direkte Mapping                  | normalStep: Integer                    |   |                                    |
|                 | ntctrl(i)      | direkte Mapping                  | regulationStatus: Boolean              |   |                                    |
|                 | l_cont(i)      | direkte Mapping                  | tcUControlMode: TransformerControlMode |   |                                    |
| TypTr2          | t2ldc(i)       | nicht relevant/ mögliche Mapping | "1"                                    | RatioTapChanger   |                                    |
|                 | llqph(i)       | nicht relevant/ mögliche Mapping | "6"                                    |   |                                    |
|                 | lmldc(a)       | nicht relevant/ mögliche Mapping | "v"                                    |   |                                    |
|                 | uset_mode(i)   | nicht relevant/ mögliche Mapping | "0"                                    | TapChangerControl   |                                    |
|                 | usetp(r)       | direkte Mapping                  | neutralU: Voltage                      |   |                                    |
|                 | tctr(r)        | direkte Mapping                  | subsequentDelay: Seconds               |   |                                    |
|                 | ldc(i)         | nicht relevant/ mögliche Mapping | "0"                                    |   |                                    |
|                 | usp_low(r)     | keine direkte Mapping            | limitVoltage: Voltage                  |   |                                    |
|                 | usp_up(r)      | keine direkte Mapping            | limitVoltage: Voltage                  |   |                                    |
|                 | TypTr2         | ID(a:40)                         | nicht relevant/ mögliche Mapping       | aliasName: String   | PowerTransformerEnd (HV and LV)    |
|                 |                | loc_name(a:40)                   | nicht relevant/ mögliche Mapping       | name: String  |                                    |
|                 |                | fold_id(p)                       | nicht relevant/ nicht gefunden         |   |                                    |
|                 |                | stn(r)                           | direkte Mapping                        | RatedS: Apparent Power<br>Global Variable of CimGrid: Frequency | PowerTransformerEnd (high voltage) |
|                 |                | trnom(r)                         | keine direkte Mapping                  |   |                                    |
|                 |                | utrn_hr(i)                       | direkte Mapping                        | ratedU: Voltage   |                                    |
| tr2cn_h(a:2)    |                | nicht relevant/ mögliche Mapping | connectionKind: String                 | PowerTransformerEnd (low voltage)                               |                                    |
| utrn_ll(r)      |                | direkte Mapping                  | ratedU: Voltage                        |   |                                    |
| tr2co_ll(a:2)   |                | nicht relevant/ mögliche Mapping | connectionKind: String                 | PowerTransformer  |                                    |
| nt2ag(r)        |                | keine direkte Mapping            | vectorGroup: String                    |   |                                    |
| TypTr2          |                | ex0h_n(r)                        | nicht relevant/ nicht gefunden         |   | RatioTapChanger                    |
|                 |                | tap_side(i)                      | keine direkte Mapping                  | aliasName: String   |                                    |
|                 |                | dutap(r)                         | direkte Mapping                        | stepVoltageIncrement: PerCent                                   |                                    |
|                 |                | phitr(r)                         | keine direkte Mapping                  | 0   |                                    |
|                 |                | mntap0(i)                        | direkte Mapping                        | neutralStep: Integer  |                                    |
|                 | ntpmn(i)       | direkte Mapping                  | lowStep: Integer                       |   |                                    |
|                 | ntpmx(i)       | direkte Mapping                  | highStep: Integer                      |   |                                    |
|                 | TypTr2         | manuf(a:20)                      | nicht relevant/ mögliche Mapping       | aliasName: String   | Asset                              |
|                 |                | chr_name(a:20)                   | nicht relevant/ mögliche Mapping       | name: String  |                                    |
|                 |                | uk0r(r)                          | nicht relevant/ mögliche Mapping       | r: Resistance   |                                    |
|                 |                | uk0tr(r)                         | short circuit test                     | x: Reactance  |                                    |
|                 |                | uktr(r)                          | short circuit test                     |   |                                    |
|                 |                | peutr(r)                         | short circuit test                     |   |                                    |
|                 |                | pktr(r)                          | open circuit test                      | b: susceptance<br>g: Conductance                                |                                    |

## CIM mapping: Example

### ■ Contents of the CIM-DGS Adapter



## Next steps

- Further development of the CIM mapping to reach a standardized adapters
- Transition from hand-implemented adapters to model-based generation using Epsilon (Eclipse Modelling Framework)
- Moving from CIM 15 based on PyCIM to the up-to-date CIM version
- Further development of the domain model, which reflects the needs and requirements of different DSOs
- Further development of the ESOSEG platform e.g. the provided flow control mechanism, security, data transportation
- Development of new use-cases, e.g. installation and operation of a grid battery system, grid optimization through switches, etc.
- Field test of the ESOSEG framework by our participating grid operators
- Project Goal: Distribution of the platform to the open-source community

## Open discussion

- How to validate the grid modelling in CIM? Is there a standard grid model in CIM format available for comparison?
- How to depict the hierarchy of grid components in XML/RDF formats (e.g. according to voltage levels)?
- Are there default values of CIM parameters (e.g. if a value is not available)?
- Which load flow solvers using the CIM formats are available in the market?
- How to serialize CIM compound classes to RDF/XML (e.g. StreetAddress)?



## Open discussion

- How to associate a ConnectivityNode to a PositionPoint (to assign a geographical position)?
- How to associate a GeneratingUnit to a Terminal?
- Is there a converter from RDF to XDF format?
- How to map physical parameter of PV GeneratingUnit (e.g. tilt angle, orientation)?
- Is there a possibility to modify type of input parameters (e.g. inputting  $S$ ,  $\cos\varphi$  to EnergyConsumer instead of  $p$ ,  $q$ )

[www.esoseg.de](http://www.esoseg.de)

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